

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

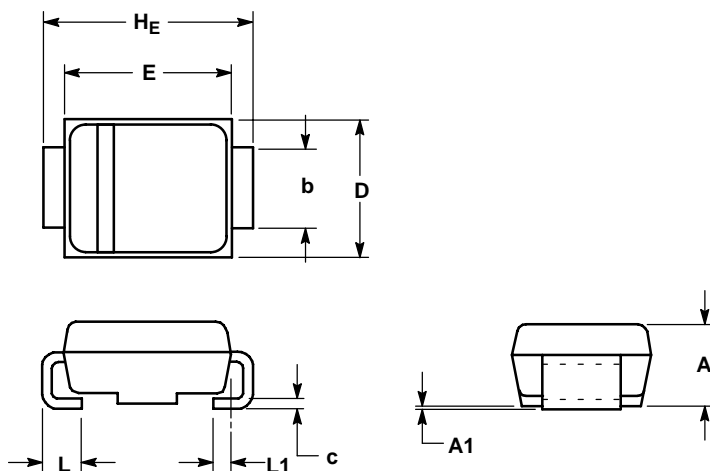
ON Semiconductor



SCALE 2:1

**SMB**  
CASE 403A-03  
ISSUE F

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## NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
<b>A</b>	1.90	2.13	2.45	0.075	0.084	0.096
<b>A1</b>	0.05	0.10	0.20	0.002	0.004	0.008
<b>b</b>	1.96	2.03	2.20	0.077	0.080	0.087
<b>c</b>	0.15	0.23	0.31	0.006	0.009	0.012
<b>D</b>	3.30	3.56	3.95	0.130	0.140	0.156
<b>E</b>	4.06	4.32	4.60	0.160	0.170	0.181
<b>HE</b>	5.21	5.44	5.60	0.205	0.214	0.220
<b>L</b>	0.76	1.02	1.60	0.030	0.040	0.063
<b>L1</b>	0.51 REF			0.020 REF		

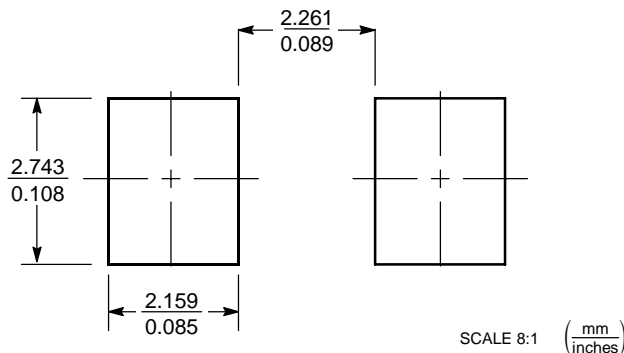
## GENERIC MARKING DIAGRAM\*



xxxx = Specific Device Code  
AL = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package  
(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.


## SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>DESCRIPTION:</b>	<b>SMB</b>	<b>PAGE 1 OF 2</b>

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